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(54) INTERRUPT CONTROLLER

(57) Abstract:

PURPOSE: To reduce the overhead of software concerning work requests, resource competition, etc. of a controlling computer or the like by discriminating an interrupt request in a high grade by a status signal and an interrupt key signal.

CONSTITUTION: When interrupt request signals 1, 3, and 5 are applied, it is discriminated whether interrupt requests are accepted or not on a basis of interrupt key signal groups 2, 4, and 6, which are set peculiarly to these interrupt request signals, and a status signal group 7. That is, with respect to the interrupt request signal 1, the interrupt is permitted independently of a status signal STATE1 when an interrupt key signal IKEYDC11 is in the high level, and the interrupt is permitted in case of the coincidence between signals STATE1 and IKEYY11 when the signal IKEYDC11 is in the low level. The same is said of signals STATE2WSTATEm; and when all of them are satisfied, a signal INTA1 is set to the high level to accept interrupts.



